

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/767,323	SANDERS, LESTER	
Examiner	Art Unit	2123	Page 1 of 1
Samuel Broda			

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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B	US-6,397,170 B1	05-2002	Dean et al.	703/14
C	US-5,838,947 A	11-1998	Sarin, Harish K.	703/14
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L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages	
U	Rodnunsky et al, "Power Estimation of CMOS Circuits via Power Software," IEEE Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 149-152 (May 1998)	
V	Macii et al, "Power Consumption of Static and Dynamic CMOS Circuits: A Comparative Study," IEEE 2nd International Conference on ASIC, pp. 425-427 (October 1996)	
W	Eisenmann et al, "Power Calculation for High Density CMOS Gate Array's," IEEE Euro ASIC '91, pp. 198-203 (May 1991)	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.